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Serial No.: 09/523,990	Art Unit 2876
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<u>20 January 2005</u>	
Date of Deposit	20 January 2005
George O. Saile Reg. No. 19572	Signature
Name of Person Making Deposit	Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:	Mou-Shiung Lin et al.	Date:	20 January 2005
Serial No.:	09/523,990 <u>09/523,990</u>	Art Unit:	2876
Filing Date:	13 March 2000	Examiner:	Daniel I. Walsh
Confirmation No.:	6198	Docket No.:	MEG99-005C
Title:	Method of Manufacture and Identification of Semiconductor Chip Marked for Identification with Internal Marking Indicia and Protection Thereof by Non-Black Layer and Device Produced Thereby	Attorney:	George O. Saile & Associates 28 Davis Avenue Poughkeepsie, New York, 12603

AMENDMENT

Commissioner for Patents
P. O. Box 1450
Alexandria VA 22313-1450

Your Honor:

In response to the Office Action of 20 October 2004, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.
Remarks/Arguments begin on page 7 of this paper.

The Commissioner is hereby authorized to charge payment of any additional fees involved with claims and the like to Deposit Account No. 19-0033

George O. Saile Reg. No. 19572
Name

George O. Saile
Signature

20 January 2005
Date

MEG99-005C